THREADS IN CHIP MULTIPROCESSORS

Inventor(s): Wang et al.

Express Mail No.: EV325525740 US

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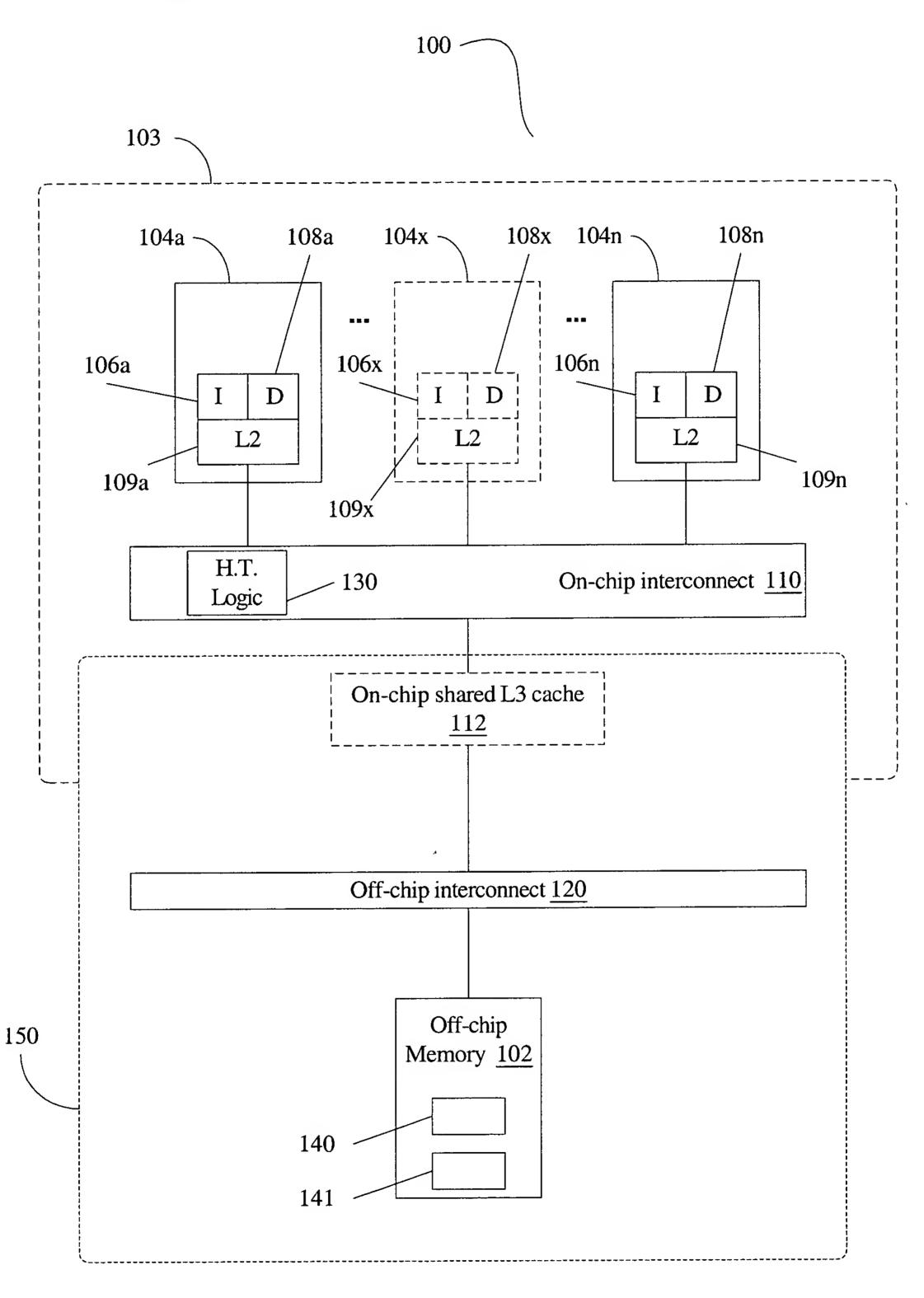


Fig. 1

Blakely Sokoloff Taylor & Zafman, LLP (503) 684-6200
Title: METHOD AND APPARATUS FOR AFFINITY-GUIDED SPECULATIVE HELPER
THREADS IN CHIP MULTIPROCESSORS

Inventor(s): Wang et al.

Express Mail No.: EV325525740 US

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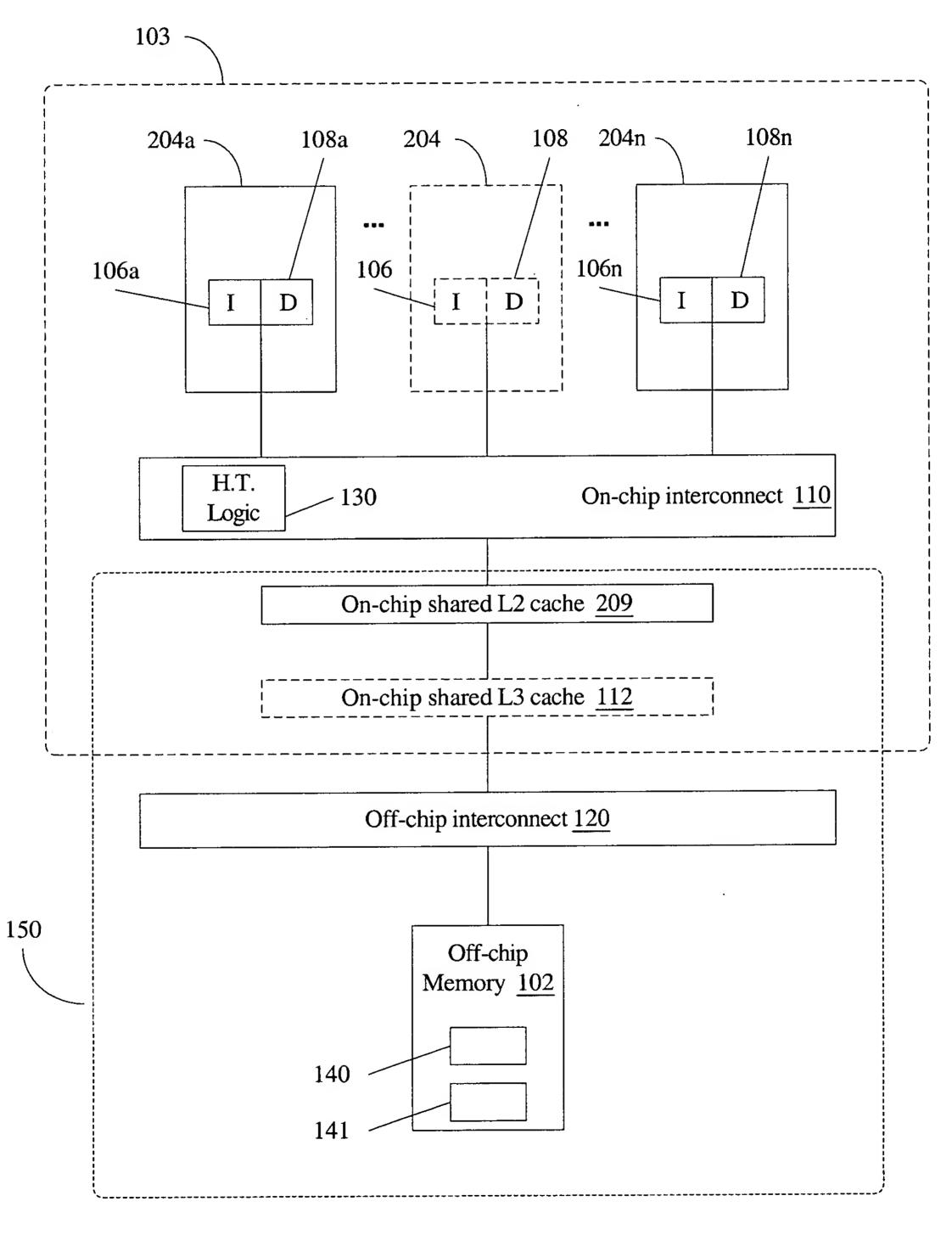


Fig. 2

Blakely Sokoloff Taylor & Zafman, LLP (503) 684-6200 Title: METHOD AND APPARATUS FOR AFFINITY-GUIDED SPECULATIVE HELPER THREADS IN CHIP MULTIPROCESSORS

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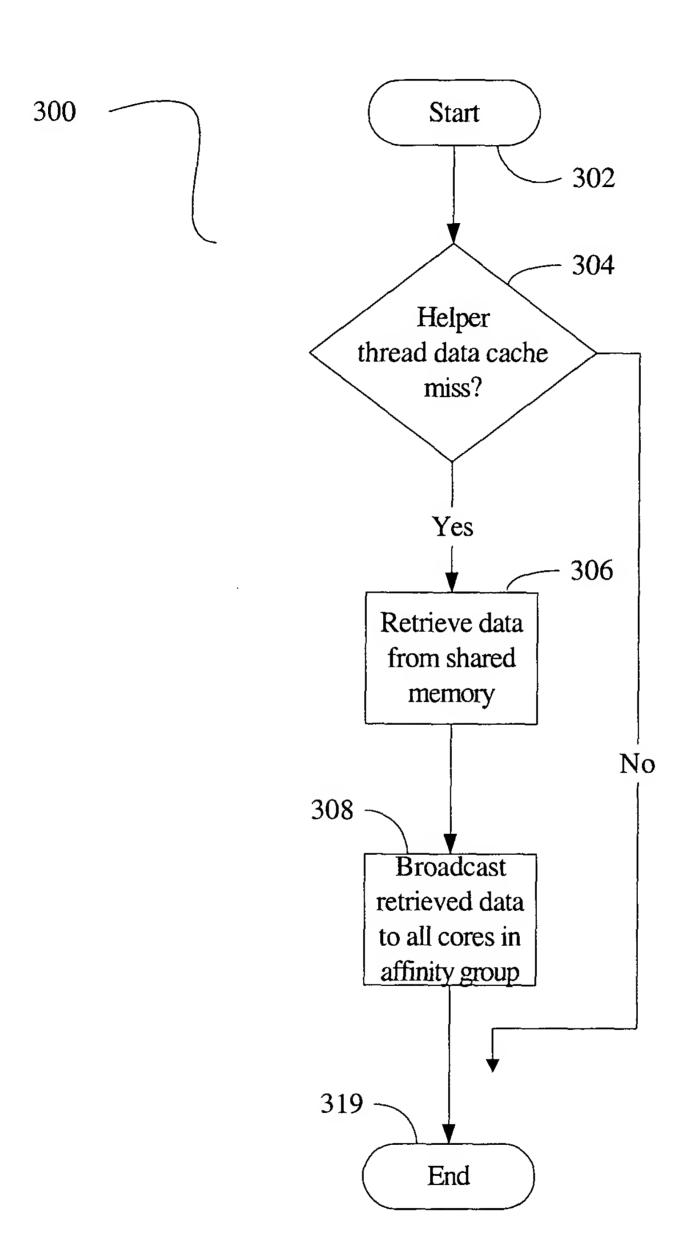


Fig. 3

Blakely Sokoloff Taylor & Zafman, LLP
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THREADS IN CHIP MULTIPROCESSORS
Inventor(s): Wang et al.
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108n 109n 105n Fig. 4 c.c. 1.2 104n 106n ... 108y 105y 110 c.c. 104x 106y 104y Off-chip interconnect 120 On-chip shared L3 cache 108x Memory 102 豆 Off-chip c.c. 17 Miss Helper Core 105x-109x 106x 108a 豆 c.c. 12 104a Main Core 109a 105a 106a 150 **Affinity** Group

THREADS IN CHIP MULTIPROCESSORS

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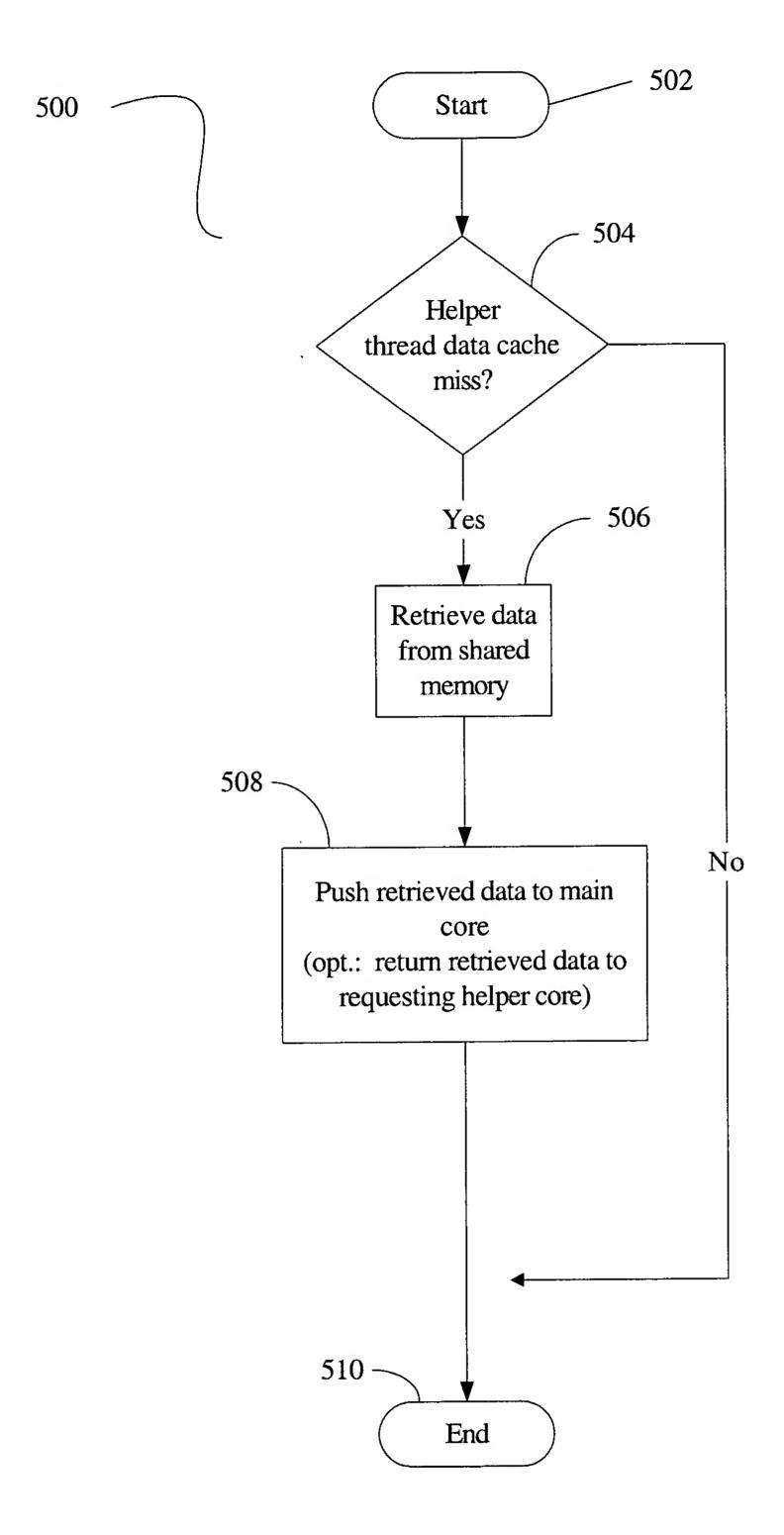


Fig. 5

Blakely Sokoloff Taylor & Zafman, LLP (503) 684-6200 Title: METHOD AND APPARATUS FOR AFFINITY-GUIDED SPECULATIVE HELPER THREADS IN CHIP MULTIPROCESSORS

Inventor(s): Wang et al.

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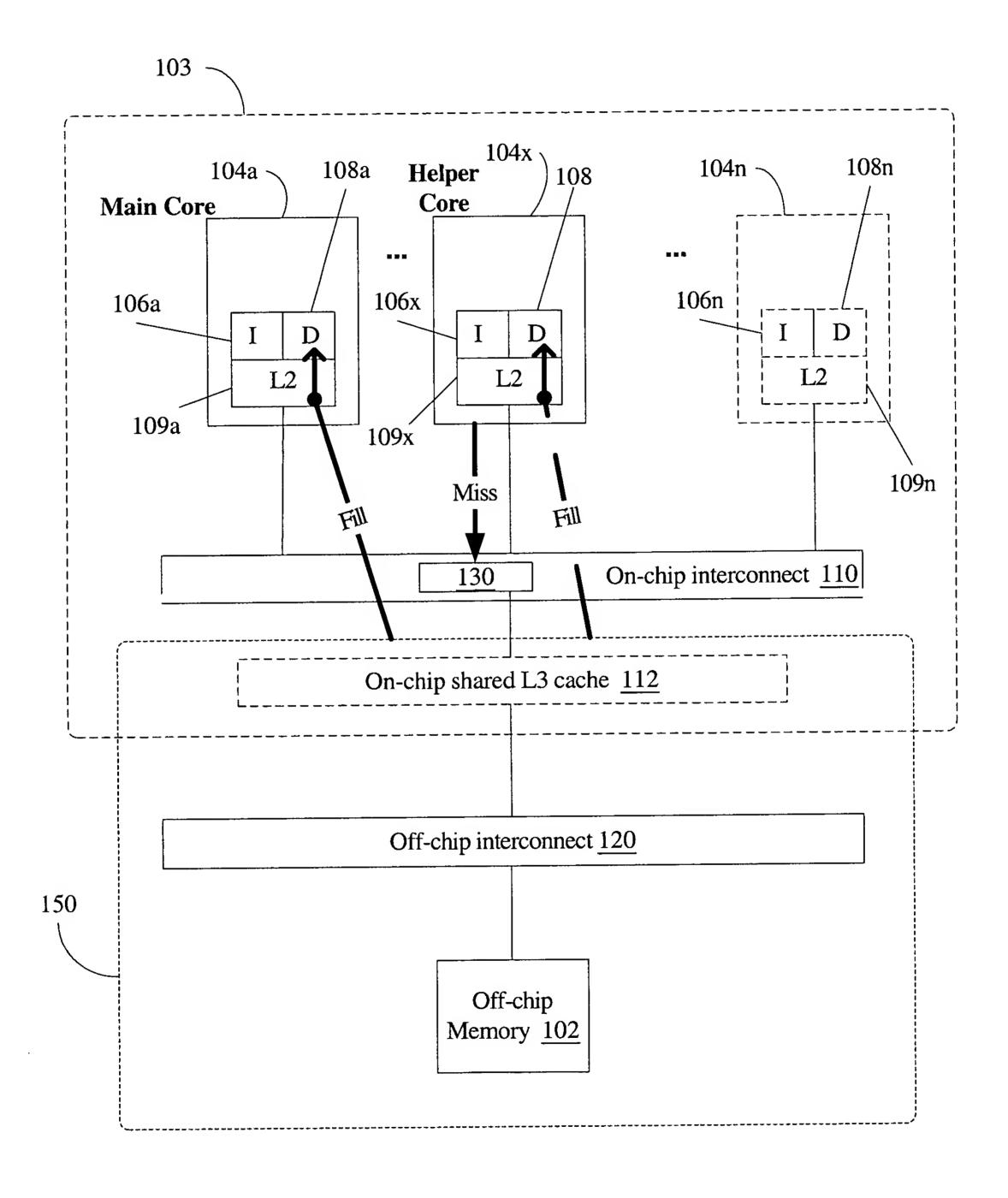


Fig. 6

Docket No.: 042390.P15449

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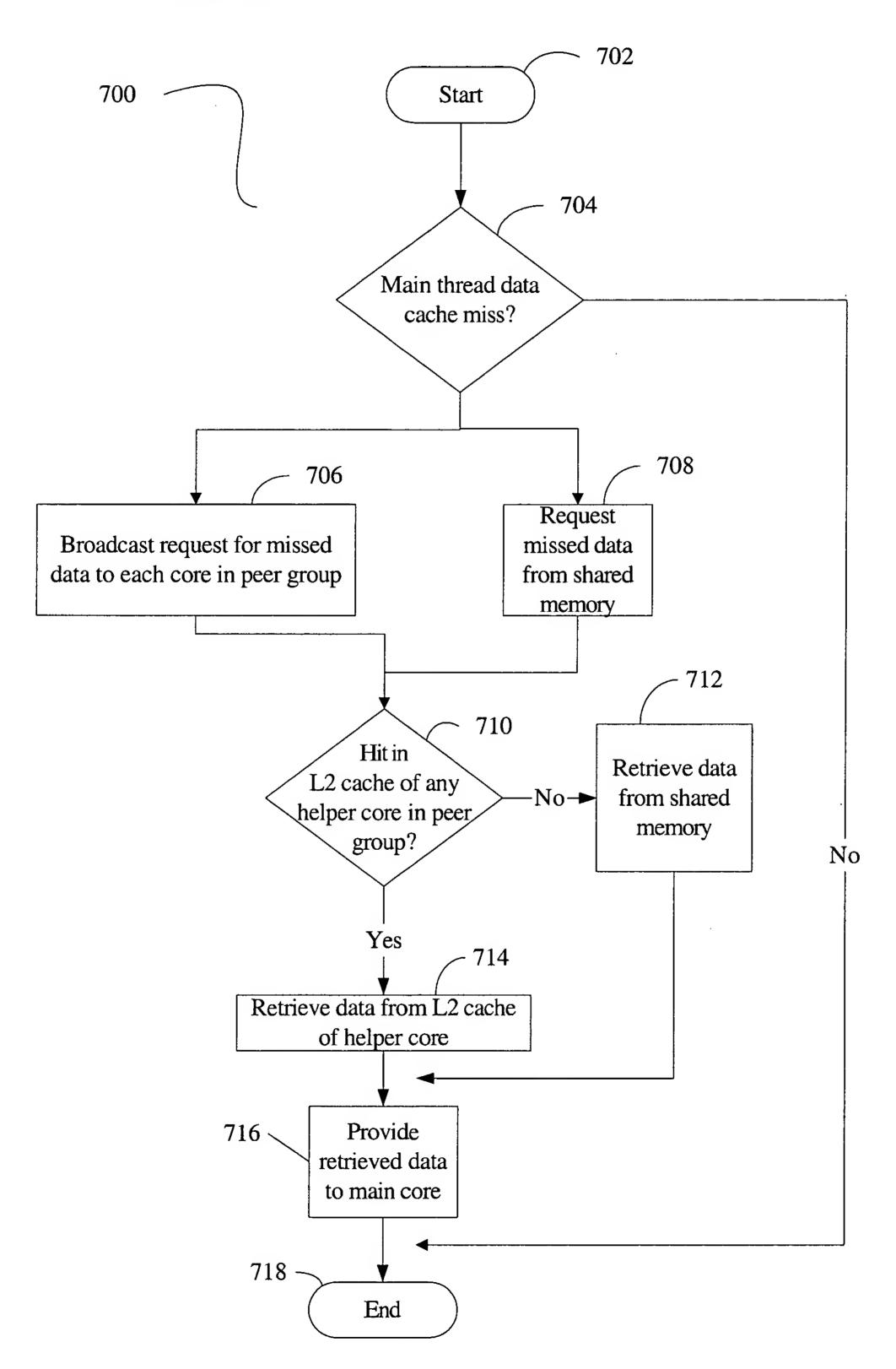


Fig. 7

THREADS IN CHIP MULTIPROCESSORS

Inventor(s): Wang et al.

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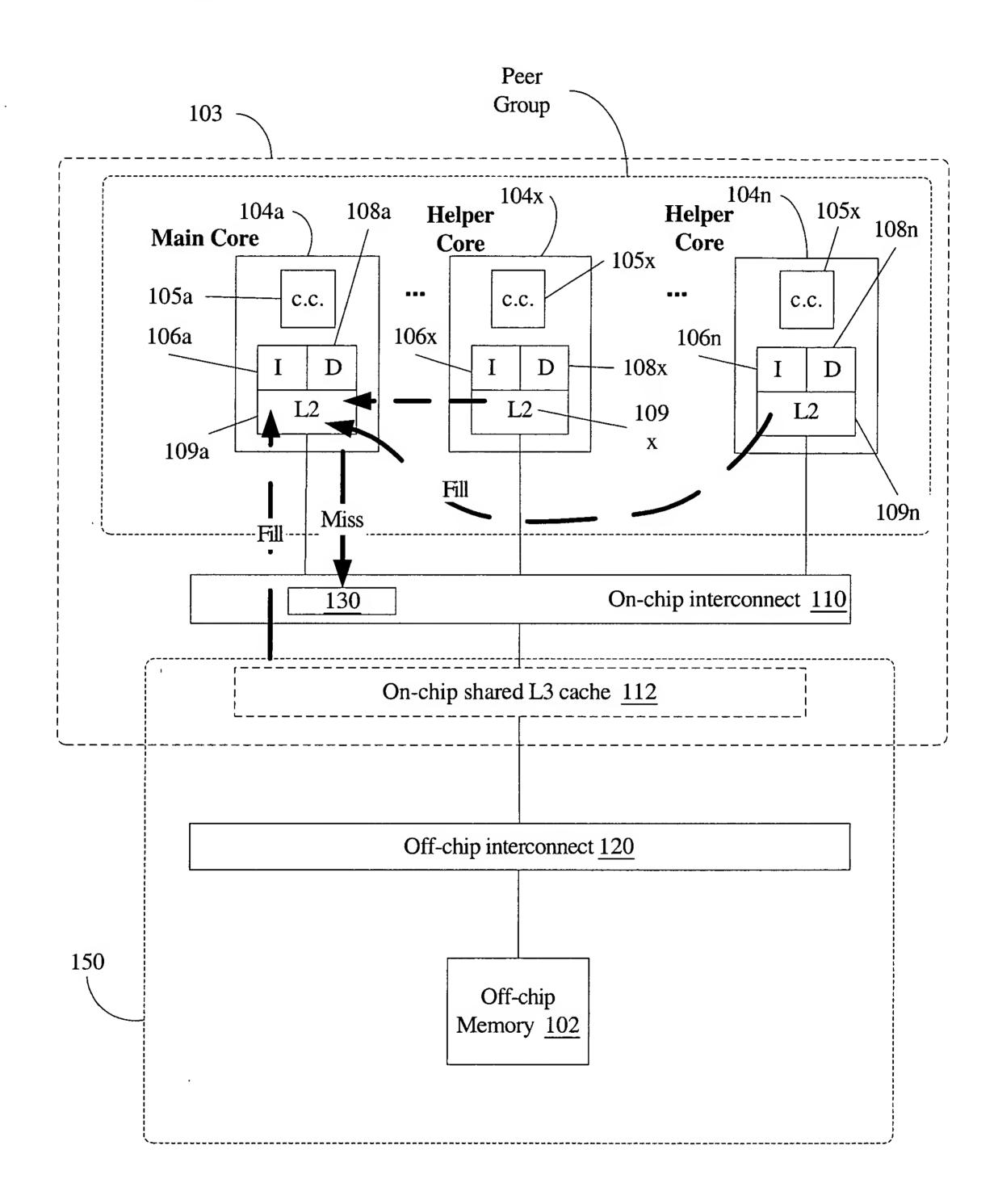


Fig. 8